

Electronic Acknowledgement Receipt

EFS ID:	1690961
Application Number:	10791268
International Application Number:	
Confirmation Number:	8944
Title of Invention:	DATA MODULATION METHOD AND APPARATUS CAPABLE OF SUPPRESSING DC COMPONENT USING PARITY INFORMATION OF SYNCHRONIZATION CODEWORD
First Named Inventor/Applicant Name:	Jae-seong Shim
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Filer Authorized By:	Michael D. Stein
Attorney Docket Number:	1793.1211
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Application Type:	Utility

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1		IDSpart1_04-17-07.pdf	5977033	yes	48

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Information Disclosure Statement (IDS) Filed	1	5
	Foreign Reference	6	30
	Foreign Reference	31	35
	NPL Documents	36	39
	NPL Documents	40	48

Warnings:

Information:

2	Foreign Reference	IDSpart2_04-17-07.pdf	8505227	no	67
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Warnings:

Information:

Total Files Size (in bytes):		14482260
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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